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Pages:521 - 525

[\[Abstract\]](#) [\[PDF Full-Text \(456KB\)\]](#) IEEE CNF
2 Controller power estimation using information from behavioral description*Surti, P.; Chao, L.-F.;*

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3 Exploiting skewed state probabilities for low power state assignment*Bhupathi, I.; Chao, L.-F.;*

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4 Efficient retiming and unfolding*Chao, L.-F.; Sha, E.H.-M.;*

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5 Static scheduling of uniform nested loops*Chao, L.-F.; Sha, E.H.-M.;*

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6 Rate-optimal static scheduling for DSP data-flow programs

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7 Efficient distributed reconfiguration for binary trees on Diogenes m

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Chao, L.-F.; Sha, E.H.-M.;

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10 Design for easily applying test vectors to improve delay fault coverage

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3 A Low cost, microprocessor compatible, 18.4 um/sup 2/,6-t bulk ce technology for high speed SRAMS

Helm, M.; Kavanaugh, W.; Liew, B-K.; Petti, C.; Stolmeijer, A.; Ben-tzur, M.; Bornstein, J.; Lilygren, J.; Ting, W.; Trammel, P.; Allan, J.; Gray, G.; Hartran Radigan, S.; Shanmugan, J.K.; Shrivastava, R.;

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1 On Steiner tree problem with 45° routing
Lee, D.T.; Shen, C.-F.; Ding, C.-L.;

Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium , Volume: 3 , 28 April-3 May 1995

Pages:1680 - 1682 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(228KB\)\]](#) **IEEE CNF**
2 A new optimization driven clustering algorithm for large circuits
Ding, C.-L.; Ho, C.-Y.; Irwin, M.J.;

Design Automation Conference, 1993, with EURO-VHDL '93. Proceedings EUR DAC '93. European , 20-24 Sept. 1993

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Pages:524 - 527[\[Abstract\]](#) [\[PDF Full-Text \(316KB\)\]](#) **IEEE CNF**

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